A servo actuator controlling system includes a master controller and a number of servo actuators coupled to at least one interface of the master controller. The master controller includes a master MCU and a number of interfaces connected to the master MCU via a first bus. Each servo actuator includes a servo MCU, a first interface coupled to the servo MCU via a second bus, a second interface coupled to the first interface and the servo MCU, a first servo switch connected between the first interface and the servo MCU, and a second servo switch connected between the second interface and the servo MCU. The first servo switch is set to turn on or off the first interface and the second servo switch is set to turn on or off the second interface.
Description

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application claims priority to Chinese Patent Application No. 201610514818.4, filed June 30, 2016, which is hereby incorporated by reference herein as if set forth in its entirety.

BACKGROUND

1. Technical Field

[0002] The present disclosure generally relates to servo control technology, and particularly to a master controller, a servo actuator, and servo controlling system having the master controller and the servo actuator.

2. Description of Related Art

[0003] Robots are widely used in our daily life and entertainment. A robot may include a master controller and a plurality of servo actuators. The plurality of servo actuators are electrically connected to the master controller via buses in series and/or in parallel. The master controller transmits control signals via buses for controlling the plurality of servo actuators to perform actions.

[0004] The buses can be Multiple Servo Motor Control Bus (MSMCB). The master controller communicates with the plurality of servo actuators via the MSMCB. The master controller sends a command to the plurality of servo actuators via the MSMCB. The plurality of servo actuators send a feedback signal to the master controller, thus the master controller can obtain an operating state of the servo actuator.

[0005] Each servo actuator has an ID, and the master controller can selectively communicate with one servo according to the ID. The ID can be stored in an Electrically Erasable Programmable Read-Only Memory (EEPROM) of the servo actuator. Each of the plurality of servo actuators includes an output interface and an input interface. The input interface is electrically coupled to the power line (VDD, not shown) and a ground line (GND, not shown). The output interface of a previous servo actuator is connected to the input interface of a next servo actuator. The plurality of servo actuators send a feedback signal to the master controller, thus the master controller can obtain an operating state of the servo actuator.

[0006] Therefore, a need exists in the industry to overcome the described problems.

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] Many aspects of the present embodiments can be better understood with reference to the following drawings. The components in the drawings are not necessarily drawn to scale, the emphasis instead being placed upon clearly illustrating the principles of the present embodiments. Moreover, in the drawings, all the views are schematic, and like reference numerals designate corresponding parts throughout the several views.

FIG. 1 is a diagrammatic drawing of a servo actuator according to one embodiment.
FIG. 2 is a diagrammatic drawing of a master controller according to one embodiment.
FIG. 3 is a diagrammatic drawing of a master controller according to another embodiment.
FIG. 4 is a diagrammatic drawing of a servo controlling system according to one embodiment.
FIG. 5 is a flowchart of a servo actuator ID setting method according to one embodiment.
FIG. 6 is a flowchart of a servo actuator ID setting method including step S201 according to another embodiment.
FIG. 7 is a flowchart of the step S201 of FIG. 6.
FIG. 8 is a flowchart of a servo actuator ID setting method according to another embodiment.
FIG. 9 is a broadcast flowchart to a plurality of servo actuators of a master controller according to one embodiment.
FIG. 10 is a flowchart of each of the plurality of servo actuators of FIG. 9.

DETAILED DESCRIPTION

[0008] The disclosure is illustrated by way of example and not by way of limitation in the figures of the accompanying drawings, in which like reference numerals indicate similar elements. It should be noted that references to “an” or “one” embodiment in this disclosure are not necessarily to the same embodiment, and such references can mean “at least one” embodiment.

[0009] Referring to FIG. 1, a servo actuator 1 can include a Micro Controller Unit (MCU) and two interfaces. The MCU of the servos actuator 1 can be denoted as a servo MCU 13. The MCU of the servo actuator 1 can be denoted as a servo MCU 13. The servo MCU 13 is electrically coupled to the two interfaces via a first internal bus L1. In the embodiment, the servo actuator 1 can be a servo motor, which includes a speed regulating gear box, a motor, a potentiometer, and a control circuit. Each of the two interfaces is electrically coupled to a power line (VDD, not shown) and a ground line (GND, not shown). The first internal bus L1 can be a Multiple Servo Motor Control Bus (MSMCB). The MSMCB can transmit data according to Inter-Integrated Circuit (I2C) protocol or Universal Asynchronous Receiver and Transmitter (UART) proto-
The two interfaces are labeled as a first interface 11 and a second interface 12. The first interface 11 and the second interface 12 can serve as an output interface or an input interface.

The servo actuator 1 can further include a first servo switch k1 and a second servo switch k2. The first servo switch k1 is electrically coupled between the first interface 11 and the servo MCU 13. The second servo switch k2 is electrically coupled between the second interface 12 and the servo MCU 13. The first switch k1 and the second switch k2 can be controlled by the servo MCU 13.

An external bus L2 (shown in FIG. 4) is connected between a master controller 2 (shown in FIG. 2) and the first interface 11 or the second interface 12 of a servo actuator 1. The servo actuator 1 can be controlled by the master controller 2 via the external bus L2.

The servo actuator 1 can further include a first connection line 14 connected between the first interface 11 and the servo MCU 13 and a second connection line 15 connected between the second interface 12 and the servo MCU 13. The servo MCU 13 can detect an access signal of the first interface 11 via the first connection line 14 and an access signal of the second interface 12 via the second connection line 15. When the first servo switch k1 and the second servo switch k2 are turned off, the servo MCU 13 can detect which of the first interface 11 and the second interface 12 is the output interface or the input interface via the first connection line 14 and the second connection line 15. When the first interface 11 receives the access signal, the first interface 11 is the input interface and the second interface 12 is the output interface. When the second interface 12 receives the access signal, the first interface 11 is the output interface and the second interface 12 is the input interface.

The servo actuator 1 can further include a storage to store an ID of the servo actuator 1. The storage can be selected from one of the group of a flash memory, a buffer, and an Electrically Erasable Programmable Read-Only Memory (EEPROM).

Referring to FIGS. 2 and 3, a master controller 2 can include a master MCU 20 and a plurality of interfaces. The number of the interfaces can be set by need. Preferably, the master controller 2 includes interfaces whose quantity is selected from one to six. A plurality of servo actuators 1 can be electrically connected to the plurality of interfaces. In the embodiment, the master controller 2 includes five interfaces, which are labeled as a first interface 21, a second interface 22, a third interface 23, a fourth interface 24, and a fifth interface 25. In one embodiment, the plurality of interfaces can be electrically coupled to a plurality of pins of the master MCU 20 via a second internal bus L3. Master switches are connected between the plurality of interfaces and corresponding pins of the master MCU 20, respectively. The master MCU 20 can simultaneously broadcast the same message or different messages to the plurality of interfaces. In one embodiment, the plurality of interfaces can be electrically coupled to one pin of the master MCU 20 as shown in FIG. 3. A master switch is connected between each of the plurality of interfaces and the pin of the master MCU 20. The master MCU 20 can broadcast the same message to the plurality of interfaces at a time.

The master switches are arranged in the second internal bus L3, and a control terminal of each master switch is electrically connected to the master MCU 20. The master MCU 20 can control on and off of the master switches. Specially, a first master switch 2a is arranged in the second internal bus L3 and connected to the first interface 21, a second master switch 2b is arranged in the second internal bus L3 and connected to the second interface 22, a third master switch 2c is arranged in the second internal bus L3 and connected to the third interface 23, a fourth master switch 2d is arranged in the second internal bus L3 and connected to the fourth interface 24, and a fifth master switch 2e is arranged in the second internal bus L3 and connected to the fifth interface 25. Control terminals of the first master switch 2a, the second master switch 2b, the third master switch 2c, the fourth master switch 2d, and the fifth master switch 2e are electrically connected to the master MCU 20.

FIG. 4 shows a servo controlling system including the master controller 2 and the plurality of servo actuators 1. The plurality of servo actuators 1 are electrically coupled to the master controller 2 in a plurality of branches. Each branch of the servo actuators 1 is electrically coupled to one interface of the master controller 2. The number of the each branch of the servo actuators 1 can be set by need. In the embodiment, the first interface 21 is electrically coupled to two servo actuators 1; the second interface 22 is electrically coupled to two servo actuators 1; the third interface 23 is electrically coupled to three servo actuators 1; the fourth interface 24 is electrically coupled to n servo actuators, which are labeled as a first servo actuator 1a, a second servo actuator 1b, a third servo actuator 1c, and a nth servo actuator 1n; and the fifth interface 25 is electrically coupled to three actuators 1.

The first servo actuator 1a, the second servo actuator 1b, the third servo actuator 1c, and the nth servo actuator 1n are coupled to the fourth interface 24 via the bus L1 in series. In detail, one of the two interfaces (e.g. the first interface 11, which is served as the input interface) of the first servo actuator 1a is connected to the fourth interface 24, and the other of the two interfaces (e.g. the second interface 12, which is served as the output interface) is serially connected to one of the two interfaces (e.g. the first interface 11, which is served as the input interface) of the second servo actuator 1b, and so on. Thus, the plurality of servos actuators 1a to 1n are coupled to the master controller 2 in series.

During ID setting process of the plurality of servo actuators, the servo MCU 13 of a current servo actuator determines whether the first interface is the input interface or the output interface via the first connection
In the embodiment, the master controller 2 and each of the plurality of servo actuators 1 can function as one node, and the master controller 2 is a master node, each of the plurality of servo actuators 1 is a slave node.

In the embodiment, the master controller 2 and each of the plurality of servo actuators 1 can function as one node, and the master controller 2 is a master node, each of the plurality of servo actuators 1 is a slave node.

In step S101, servo actuators disconnecting step: the plurality servo actuators connected to the plurality of interfaces of the master controller.

In step S102, servo actuator ID setting step: the master controller 2 broadcasts a message to the bus L3. The message indicates that an original ID is replaced with a target ID. The servo MCU 13 of the servo actuator which is directly connected to the interface of the master controller 2 detects which of the first interface and the second interface is the input interface via the first connection line 14 and the second connection line 15. If the first interface is the input interface, the second interface is the output interface. If the first interface is the output interface, the first interface is the output interface. The servo switch connected to the input interface is turned on and the servo actuator 1 is connected to the master controller 2. Take the fourth interface 24 for example, the message is received by the first servo actuator 1a, the original ID of the first servo actuator 1a is replaced by the target ID. And then the servo switch connected to the output interface is turned on, such that the second servo actuator 1b can receive the message from the master controller 2. The step S102 is repeatedly performed until all servo actuators ID of the servo actuators 1 are set. The target ID of each of the plurality of servo actuators is unique.

In the embodiment, the target ID of the first servo actuator 1a is set as 1#; the target ID of the second servo actuator 1b is set as 2#; the target ID of the third servo actuator 1c is set as 3#; ... and the target ID of the nth servo actuator 1n is set as n#.

FIG. 6 shows a flowchart of a servo actuator ID setting method according to another embodiment. Depending on the embodiment, additional steps may be added, others removed, and the ordering of the steps may be changed.

The plurality of interfaces of the master controller 2 are selected to turn on in sequence, the following steps are repeatedly performed to set servo actuators ID of the servo actuators connected to the plurality of interfaces of the master controller.

In step S201, default disconnected ID setting
step: the master controller 2 broadcasts a first message to all servo actuators connected to the plurality of interfaces of the master controller 2, which denotes that the servo actuator is disconnected from the master controller 2. The first message indicates that an original ID is replaced with a default disconnected ID. The original ID of each of the plurality of servo actuators is replaced with the default disconnected ID according to the message. The default disconnected ID is stored in the storage. When a servo actuator ID is the default disconnected ID, the servo switch connected to the input interface is set to be turned on and the servo switch connected to the output interface is set to be turned off. When the servo actuator ID is different from the default disconnected ID, the two servo switches connected to the input and output interfaces are set to be turned on. The default disconnected ID is set by need. In the embodiment, the default disconnected ID is 0xFF.

[0032] In step S202, the master controller 2 broadcasts a second message indicating that the default disconnected ID is replaced with a target ID. Thus, each of the plurality of servo actuators is sequentially connected to the master controller 2. Once a servo actuator is connected to the master controller 2, the default disconnected ID of the servo actuator is replaced with the target ID. Then the two servo switches of the servo actuator are turned on, and the next servo actuator is connected to the master controller 2. The step S202 is repeated until all servo actuators ID of the servo actuators 1 are set. Each target ID of each of the plurality of servo actuators is unique.

[0033] In the embodiment, the ID of all servo actuators 1 are set as the default disconnected ID. When the servo actuator ID is the default disconnected ID, the servo switch connected to the input interface is set to be turned on and the servo switch connected to the output interface is set to be turned off. And then the master controller 2 broadcasts the second message indicating that the default disconnected ID is replaced with a target ID. Thus, each of the plurality of servo actuators sequentially receives the second message and the default disconnected ID of the servo actuator is replaced with the target ID, and the two servo switches connected to the output interfaces are turned on. The next servo actuator can receive the second message. In other words, any one servo actuator cannot connect to the master controller before the ID of previous servo actuator is set.

[0034] In an initial state, take the fourth interface 24 for example, only the servo switch connected to the input interface of the first servo actuator 1a is turned on; the first servo actuator 1a is electrically coupled to the master controller 2 to receive the second message. The servo actuators 1 after the first servo actuator 1a cannot connect to the master controller 2. When the default disconnected ID of the first servo actuator 1a is replaced with the target ID, the servo switch connected to the output interface of the first servo actuator 1a is turned on. And the default disconnected ID of the second servo actuator 1b is replaced by the target ID in a similar manner with the first servo actuator 1a.

[0035] In step S2011, the master controller 2 broadcasts a first message indicating an original ID is replaced with a non-default-disconnected ID. The non-default-disconnected ID is different from the default disconnected ID, and denotes that the servo actuator can communicate with the master controller 2. The original ID of each of the plurality of servo actuators is replaced with the non-default-disconnected ID according to the first message. The master controller 2 reads back and checks whether there is a default disconnected ID in the bus L1. In other words, the master controller 2 reads back and checks whether a servo actuator ID is the default disconnected ID. If the servo actuator ID is the default disconnected ID, the master controller 2 keeps broadcasting the first message until all servo actuators ID are set as the non-default-disconnected ID. The non-default-disconnected ID can be set by need, which is different with the default disconnected ID. In the embodiment, the non-default-disconnected ID is set as 0xFE. The two servo switches of each of the plurality of servo actuators are set to be turned on, and each of the plurality of servo actuators can receive the message sent by the master controller 2.

[0036] In step S2022, the master controller 2 broadcasts a second message indicating the non-default-disconnected ID is replaced with a default disconnected ID. The non-default-disconnected ID of each of the plurality of servo actuators is replaced with the default disconnected ID according to the second message.

[0037] In step S2022, the master controller 2 broadcasts a second message indicating the non-default-disconnected ID is replaced with a default disconnected ID. The non-default-disconnected ID of each of the plurality of servo actuators is replaced with the default disconnected ID according to the second message.

[0038] FIG. 8 shows a flowchart of a servo actuator ID setting method according to another embodiment. Depending on the embodiment, additional steps may be added, others removed, and the ordering of the steps may be changed. The servo actuator ID setting method of FIG. 8 is similar to the servo actuator ID setting method of FIG. 5 except that the step S201 is replaced with the steps S2011 and S2012 of FIG. 6.

[0039] The plurality of master switches of the interfaces of the master controller 2 are selected to turn on in sequence, the following steps are repeatedly performed to set servo actuators ID of the servo actuators connected to the plurality of interfaces of the master controller. In the embodiment, referring also to FIGs. 2-4, when the first master switch 2a of the first interface 21 is set to be turned on, the other master switches of remaining interfaces are set to be turned off; the servo actuators ID of the servo actuators connected to the first interface 21 are set. Similarly, when the second master switch 2b of the second interface 22 is set to be turned on, the other master switches of remaining interfaces are set to be turned off; the servo actuators ID of the servo actuators con-
FIG. 9 shows a broadcast flowchart to a plurality of servo actuators connected to the second interface 22 are set. When the third master switch 2c of the third interface 23 is set to be turned on, the other switches of remaining interfaces are set to be turned off; the servo actuators ID of the servo actuators connected to the second interface 22 are set. When the fourth master switch 2d of the fourth interface 24 is set to be turned on, the other switches of remaining interfaces are set to be turned off; the servo actuators ID of the servo actuators connected to the fourth interface 24 are set. When the fifth master switch 2e of the fifth interface 25 is set to be turned on, the other switches of remaining interfaces are set to be turned off; the servo actuators ID of the servo actuators connected to the fifth interface 25 are set. A switching order of the plurality of servo actuators is set as 0xFE.

In step S301, the master controller 2 broadcasts a first message indicating an original ID is replaced with a non-default-disconnected ID. The original ID of each of the plurality of servo actuators is replaced with the non-default-disconnected ID according to the first message. The master controller 2 reads back and checks whether there is a default disconnected ID in the bus L1. In other words, the master controller 2 reads back and checks whether a servo actuator ID is the default disconnected ID. When the servo actuator ID is the default disconnected ID, the master controller 2 keeps broadcasting the first message until the all servo actuators ID are set as the non-default-disconnected ID, and step S302 is performed. In the embodiment, the non-default-disconnected ID is set as 0xFE.

In step S302, the master controller 2 broadcasts a second message indicating the non-default-disconnected ID is replaced with a default disconnected ID. The non-default-disconnected ID of each of the plurality of servo actuators is replaced with the default disconnected ID according to the second message. In the embodiment, the default disconnected ID is 0xFF.

In step S303, the master controller 2 broadcasts a third message indicating that the default disconnected ID is replaced with a target ID. The default disconnected ID of each of the plurality of servo actuators is replaced with the target ID. The step S303 is repeatedly performed until all servo actuators ID of the plurality of servo actuators 1 are replaced with the target ID. When the master controller broadcasts the third message, the master controller 2 reads back and checks whether the default disconnected ID of the one servo actuator which can receive the third message is replaced with the target ID. When the default disconnected ID of the one servo actuator is replaced with the target ID, the master controller 2 broadcasts the third message to the next servo actuator. The target ID of each of the plurality of servo actuators is unique. Take the fourth interface 24 for example, when the default disconnected ID (0xFF) of the first servo actuator 1a is replaced with the target ID (0x01), the master controller 2 broadcasts the third message including the target ID (0x02) to the next servo actuator. Preferably, the target ID increases or decreases in order. Such as, the target ID increases from 0x01, 0x02,... to OxOn or decreases from OxOn,... to 0x02, 0x01.

FIG. 10 shows a flowchart of each of the plurality of servo actuators of FIG. 9.

In step S401, reading the servo actuator ID stored in the storage by the servo MCU 13.

In step S402, determining whether the servo actuator ID is the default disconnected ID. When the servo actuator ID is not the default disconnected ID, step 403 is performed; when the servo actuator ID is the default disconnected ID, step 404 is performed.

In step S403, turning on the first servo switch k1 and the second servo switch k2.

In step S404, detecting whether the first connection line 14 and the second connection line 15 has an access signal, and step S405 and S406 are performed.

In step S405, determining whether the first connection line 14 receives the access signal. When the first connection line 14 doesn’t receive the access signal, step S406 is performed; when the first connection line 14 receives the access signal, step 407 is performed.

In step S406, turning off the first servo switch k1.

In step S407, turning on the first servo switch k1.

In step S408, determining whether the second connection line 15 receives the access signal. When the second connection line 15 doesn’t receive the access signal, step S409 is performed; when the second connection line 15 receives the access signal, step S410 is performed.

In step S409, turning on the second servo switch k2.

In step S410, turning off the second servo switch k2.

In step S403, when the current servo actuator ID is not the default disconnected ID, the first and second servo switches are set to be turned on, such that the current servo actuator 1 can receive the message broadcasted by the master controller 2. And then the servo actuator 1 can perform a certain action according to the message broadcasted by the master controller 2.

In steps S404 to S410, when the servo actuator ID of the one servo actuator 1 is the default disconnected ID, the servo MCU 13 detects which of the first interface and the second interface is the input interface or the out-
put interface via the first connection line 14 and the second connection line 15. The first interface is the input interface and the second interface is the output interface or the first interface is the output interface and the second interface is the input interface. And then the servo switch of the input interface is set to be turned on and the servo switch of the output interface is set to be turned off. Such that the one servo actuator can connect to the master controller via the bus L1 and perform the certain action, such as setting the target ID. When the first interface is the input interface, the first servo switch k1 is set to be turned on, and the second servo switch k2 is set to be turned off.

[0057] In the embodiment, when the step S301 is performed by the master controller 2, the steps S401-S403 are simultaneously performed by the servo actuators connected to the one interface of the master controller 2. As the original IDs of most of the plurality of servo actuators are not the default disconnected ID (e.g. 0xFF), such that each of the plurality of servo actuators receives the broadcasted message from the master controller 2 and the original ID is replaced with the non-default-disconnected ID (e.g. 0xFE). Even if the original ID of one part of the plurality of servo actuators are the default disconnected ID, the default disconnected ID is replaced with the non-default-disconnected ID via steps S401, S402, and S404-S410, the next servo actuators can receive the broadcasted message from the master controller to replace the original ID with the non-default-disconnected ID. The original IDs of the all servo actuators are replaced with the non-default-disconnected ID.

[0058] The step S302 is performed by the master controller 2. The master controller 2 broadcasts the message to the all servo actuators via the external bus L2, such that the non-default-disconnected IDs of all servo actuators are replaced with the default disconnected ID. When the non-default-disconnected-ID of one servo actuator is replaced with the default disconnected ID, in a next cycle, the steps S401, S402, and S404-S410 are performed, such that the servo switch connected to the input interface is set to be turned on and the servo switch connected to the output interface is set to be turned off. Thus, only the first servo actuator connected to any one of the plurality of interfaces of the master controller 2 can receive the broadcasted message from the master controller 2.

[0059] The step S303 is performed by the master controller 2. The master controller 2 broadcasts the third message to the first internal bus L1. When the master controller 2 broadcasts the third message first time to replace the default disconnected ID with the target ID, only the first servo actuator 1 can receive the third message. The steps S401, S402, S404-S410 are performed by the first servo actuator 1, the default disconnected ID is replaced with the target ID (e.g. 0x01). The master controller 2 reads back and checks that the ID setting process of the first servo actuator 1 is completed. And in a next cycle of the broadcast, the master controller 2 broadcasts a next third message which is different from a last third message having the target ID (e.g. 0x02), the steps S401-S403 are performed by the first actuator 1, but the target ID (0x01) is not replaced. The steps S401, S402, and S404-S410 are performed by the second actuator 1, the default disconnected ID of the second servo actuator 1 is replaced with the target (0x02). The master controller 2 reads back and checks that the ID setting process of the second servo actuator 1 is completed. And in a next cycle of the broadcast, the master controller 2 broadcasts a next third message which is different from the last third message having the target ID (e.g. 0x03), the steps S401-S403 are performed by the first and second actuators, but the target ID (0x01 and 0x02) is not be replaced. The steps S401, S402, and S404-S410 are performed by the third actuator 1, the default disconnected ID of the third servo actuator 1 is replaced with the target (0x03). The master controller 2 keeps broadcasting the third message until the all servo actuators are set as the target IDs.

[0060] In the servo actuator ID setting method, the master controller 2 broadcasts the messages, and each of the plurality of servo actuators can automatically complete ID setting according to the messages.

[0061] Although the features and elements of the present disclosure are described as embodiments in particular combinations, each feature or element can be used alone or in other various combinations within the principles of the present disclosure to the full extent indicated by the broad general meaning of the terms in which the appended claims are expressed.

Claims

1. A master controller (2), comprising:
   a micro control unit (MCU) (20);
   a plurality of interfaces (21, 22) connected to the MCU (20) via a bus (L3); and
   a plurality of master switches (2a, 2b);

   wherein each of the plurality of master switches (2a, 2b) is arranged between one interface and the MCU (20) in the bus; a control terminal of each of the plurality of master switches (2a, 2b) is electrically coupled to the MCU (20).

2. The master controller of claim 1, wherein the plurality of interfaces (21, 22) are coupled to a plurality of pins of the MCU (20), each of the plurality of pins is coupled to a corresponding interface, and one master switch is arranged between each of the plurality of pins and the corresponding interface.

3. The master controller of claim 1, wherein the plurality of interfaces (21, 22) are coupled to one pin of the MCU (20), and one master switch is arranged be-
between each of the plurality of interfaces (21, 22) and the one pin.

4. A servo actuator, comprising:
   a servo micro control unit (MCU) (20);
   a first interface (11) coupled to the servo MCU (20) via a bus (L1);
   a second interface (12) coupled to the first interface (11) and the servo MCU (20) via the bus;
   a first servo switch (k1) connected between the first interface and the servo MCU (20) to turn on or off the first interface (11);
   a second servo switch (k2) connected between the second interface (12) and the servo MCU (20) to turn on or off the second interface;
   a first connection line (14) connected between the first interface and the servo MCU (20); and
   a second connection line (15) connected between the second interface and the servo MCU (20).

5. The servo actuator of claim 4, wherein a control terminal of the first servo switch (k1) and a control terminal of the second servo switch (k2) are coupled to the servo MCU (20).

6. The servo actuator of claim 4, further comprising a storage to store a servo actuator ID.

7. The servo actuator of claim 6, wherein the storage is selected from one of the group of an EEPROM, a buffer, and a flash memory.

8. The servo actuator of claim 4, wherein the bus is a Multiple Servo Motor Control Bus.

9. A servo actuator controlling system, comprising:
   a master controller (2) comprising:
   a master MCU (20) and a plurality of interfaces (21, 22) connected to the master MCU (20) via a first bus (L3); and
   a plurality of servo actuators (1) coupled to at least one interface of the master controller (2), each of the plurality of servo actuators (1) comprising:
   a servo MCU (13);
   a first interface (11) coupled to the servo MCU (13) via a second bus (L1);
   a second interface (12) coupled to the first interface (11) and the servo MCU (13) via the second bus (L1);
   a first servo switch (k1) connected between the first interface (11) and the servo MCU (13) to turn on or off the first interface;
   a second servo switch (k2) connected between the second interface (12) and the servo MCU (13) to turn on or off the second interface;
   a first connection line (14) connected between the first interface (11) and the servo MCU (13); and
   a second connection line (15) connected between the second interface (12) and the servo MCU (13).

10. The servo actuator controlling system of claim 9, wherein each of the plurality of interfaces is coupled to at least one servo actuator.

11. The servo actuator controlling system of claim 9, wherein the master controller (2) comprises a plurality of master switches (2a, 2b), and each of the plurality of master switches (2a, 2b) is arranged between one interface and the master MCU (20) in the bus; a control terminal of each of the plurality of master switches (2a, 2b) is electrically coupled to the master MCU (20).

12. The servo actuator controlling system of claim 9, wherein a control terminal of the first servo switch (k1) and a control terminal of the second servo switch (k2) are coupled to the servo MCU (13).

13. The servo actuator controlling system of claim 9, wherein each of the plurality of servo actuators (1) comprises a storage to store a servo actuator ID.

14. The servo actuator controlling system of claim 13, wherein the storage is selected from one of the group of an EEPROM, a buffer, and a flash memory.

15. The servo actuator controlling system of claim 9, wherein the first and second buses (L3, L1) are Multiple Servo Motor Control Bus.
FIG. 1
FIG. 3
FIG. 5

---

Servo actuators disconnecting step

Servo actuator ID setting step

---

$S101$

$S102$
FIG. 6

- Default disconnected ID setting step
- Replacing the default disconnected ID with a target ID
Non-default disconnected ID setting step

Replacing the non-default disconnected ID with the default disconnected ID

FIG. 7
Non-default disconnected ID setting step

Replacing the non-default disconnected ID with the default disconnected ID

Replacing the default disconnected ID with a target ID

FIG. 8
The master controller broadcasts a first message indicating an original ID is replaced with a non-default disconnected ID.

The master controller broadcasts a second message indicating the non-default disconnected ID is replaced with a default disconnected ID.

The master controller broadcasts a third message indicating that the default disconnected ID is replaced with a target ID.

FIG. 9
Reading the servo actuator ID stored in the storage.

Determining whether the servo actuator ID is the default ID

Turning on the first and second servo actuators

Detecting the first and second connection lines.

Determining whether the first line receives the access signal

Turning off the first servo switch

Turning on the first servo switch.

Determining whether the second line receives the access signal

Turning on the second servo switch.

Turning off the second servo switch.

FIG. 10
## DOCUMENTS CONSIDERED TO BE RELEVANT

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of document with indication, where appropriate, of relevant passages</th>
<th>Relevant to claim</th>
<th>CLASSIFICATION OF THE APPLICATION (IPC)</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>* paragraph [0038] - paragraph [0057]; figures 5, 6, 9 *</td>
<td>4-15</td>
<td></td>
</tr>
</tbody>
</table>

**TECHNICAL FIELDS SEARCHED (IPC)**

G05B

---

The present search report has been drawn up for all claims.

**Place of search**

The Hague

**Date of completion of the search**

27 October 2017

**Examiner**

Tsirigkas, Dimitrios

---

**CATEGORY OF CITED DOCUMENTS**

- **T**: theory or principle underlying the invention
- **E**: earlier patent document, but published on, or after the filing date
- **D**: document cited in the application
- **L**: document cited for other reasons
- **G**: member of the same patent family, corresponding document
- **P**: intermediate document
- **X**: particularly relevant if taken alone
- **A**: technological background
- **Y**: particularly relevant if combined with another document of the same category
- **O**: non-written disclosure
CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing claims for which payment was due.

☐ Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s):

☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet B

☐ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.

☐ As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.

☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:

☐ None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:

☐ The present supplementary European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims (Rule 164 (1) EPC).
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-3
   A generic microcontroller that can be used to control any piece of equipment connecting to its interfaces.

2. claims: 4-15
   A servo actuator with its own microcontroller and two interfaces that might interface it to anything (e.g. sensors).
This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on 27-10-2017. The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

<table>
<thead>
<tr>
<th>Patent document cited in search report</th>
<th>Publication date</th>
<th>Patent family member(s)</th>
<th>Publication date</th>
</tr>
</thead>
<tbody>
<tr>
<td>US 2006155900 A1</td>
<td>13-07-2006</td>
<td>CN 101506786 A</td>
<td>12-08-2009</td>
</tr>
<tr>
<td></td>
<td></td>
<td>CN 102645903 A</td>
<td>22-08-2012</td>
</tr>
<tr>
<td></td>
<td></td>
<td>DK 1960892 T3</td>
<td>13-06-2016</td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP 1960892 A2</td>
<td>27-08-2008</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ES 2575988 T3</td>
<td>04-07-2016</td>
</tr>
<tr>
<td></td>
<td></td>
<td>JP 2009518728 A</td>
<td>07-05-2009</td>
</tr>
<tr>
<td></td>
<td></td>
<td>JP 2013061998 A</td>
<td>04-04-2013</td>
</tr>
<tr>
<td></td>
<td></td>
<td>KR 20080077665 A</td>
<td>25-06-2008</td>
</tr>
<tr>
<td></td>
<td></td>
<td>US 2006155900 A1</td>
<td>13-07-2006</td>
</tr>
<tr>
<td></td>
<td></td>
<td>US 2007255879 A1</td>
<td>01-11-2007</td>
</tr>
<tr>
<td></td>
<td></td>
<td>WO 2007135485 A2</td>
<td>29-11-2007</td>
</tr>
</tbody>
</table>

| US 2009128080 A1                       | 21-05-2009       | NONE                     |                 |

For more details about this annex: see Official Journal of the European Patent Office, No. 12/82.
REFERENCES CITED IN THE DESCRIPTION

This list of references cited by the applicant is for the reader’s convenience only. It does not form part of the European patent document. Even though great care has been taken in compiling the references, errors or omissions cannot be excluded and the EPO disclaims all liability in this regard.

Patent documents cited in the description

• CN 201610514818 [0001]