EMBEDDED CAPACITIVE TOUCH PANEL, DISPLAY, AND MANUFACTURING METHOD

The present invention discloses an In-Cell capacitive touch panel, a display device and a method for manufacturing the In-Cell capacitive touch panel. The transmittance uniformity is improved and the slots are hidden by arranging the slots of the transparent conductive layer above the light-impermeable regions of the black matrix and/or below the color resistors of the same color, thereby avoiding the display of the pattern of the slots and improving the display effect.
Description

Technical Field

[0001] The present invention relates to the field of touch display technologies, in particular to an In-Cell capacitive touch panel, a display device and a method for manufacturing the In-Cell capacitive touch panel.

Technical Background

[0002] A touch screen built in a Liquid Crystal Display (LCD) device can be integrated with a color filter substrate (CF substrate) together to reduce the thickness of an entire display device and simplify the manufacturing process of the display device effectively, and hence is pursued greatly by manufacturers. As shown in Figures 1 and 2, an In-Cell touch panel includes a color filter substrate (CF substrate) 10, a black matrix 11, a touch layer 12 (which includes a metal grid 121, a transparent conductive layer 122, a first insulation layer 123 and a metal bridge 124), color resistors (R, G, B) 13, a second insulation layer 14, a Thin Film Transistor substrate (TFT substrate) 20, a circuit electrode 21 on the TFT substrate 20, and a liquid crystal layer 30. A plurality of light-permeable regions are provided on the black matrix (BM) 11 located on the CF substrate 10. Driving lines and sensing lines provided on the touch layer 12 are arranged to intersect with each other, and a touch signal is generated when intersections between the driving lines and the sensing lines are touched by the finger of a user. The color resistors 13 located on the touch layer 12 cover the light-permeable regions of the black matrix 11, and the second insulation layer 14 is located on the color resistors 13.

[0003] As shown in Figure 2, the driving lines and touching lines of the touch layer are constituted by the metal grid 121 and the transparent conductive layer 122 in the structure of the In-Cell touch panel described above, where the metal grid 121 is designed to be invisibly shielded by the black matrix, but slots of the pattern of the transparent conductive layer 122 may be visible by a person's eyes because such slots are arranged at a region in which a pixel is present (i.e. the light-permeable region). There exists a difference in transmittance of light penetrating regions with and without the transparent conductive layer 122, and such difference may be observed by the person's eyes. Since the light transmittances vary with light wavelengths (as shown in Figure 3), the obviousness degrees of the slots of the transparent conductive layer 122 observed in the case of R, G and B pictures are different from each other.

Summary of the Invention

[0004] An object of the present invention is to provide an In-Cell capacitive touch panel, a display device and a method for manufacturing the In-Cell capacitive touch panel, where the slots of the transparent conductive are arranged at suitable positions for hiding, thereby improving the display effect.

[0005] In order to solve the technical problems described above, the present invention provides an In-Cell capacitive touch panel, including: a substrate; and a black matrix, a transparent conductive layer and color resistors sequentially formed on the substrate. The black matrix is provided with a plurality of light-permeable regions, and the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, where slots of the transparent conductive layer are arranged above light-permeable regions of the black matrix and/or below the color resistors of the same color.

[0006] Preferably, the arrangement rule is that an R color resistor, a G color resistor and a B color resistor are sequentially arranged in a horizontal direction, and the color resistors of the same color are arranged in a vertical direction.

[0007] Preferably, a line width of the light-impermeable region of the black matrix in the vertical direction ranges from 7.0μm to 8.0μm.

[0008] Preferably, a line width of the light-impermeable region of the black matrix in the horizontal direction ranges from 24.5μm to 26.5μm. Preferably, horizontal slots of the transparent conductive layer are arranged above the light-impermeable regions of the black matrix, and vertical slots of the transparent conductive layer are arranged below the color resistors of the same color.

[0009] Preferably, a line width of the horizontal slot of the transparent conductive layer is 10% to 90% of the line width of the light-impermeable region of the black matrix in the horizontal direction. Preferably, the line width of the horizontal slot of the transparent conductive layer ranges from 3μm to 22μm.

[0010] Preferably, the line width of the vertical slot of the transparent conductive layer ranges from the size of a pixel subtracted by the width of the light-impermeable region of the black matrix in the vertical direction to the size of the pixel plus the line width of the light-impermeable region of the black matrix in the vertical direction.

[0011] Preferably, the line width of the vertical slot of the transparent conductive layer ranges from 33.5μm to 38.5μm.

[0012] Preferably, the In-Cell capacitive touch panel further includes a metal grid arranged between the black matrix and the transparent conductive layer.

[0013] Preferably, In-Cell capacitive touch panel further includes a first insulation layer and a metal bridge arranged between the transparent conductive layer and the color resistor.

[0014] Preferably, In-Cell capacitive touch panel further includes a second insulation layer and a photolithography spacer sequentially formed on the color resistor.

[0015] Preferably, the transparent conductive layer is made of Indium Tin Oxide (ITO), Indium Zinc Oxide (IZO), Carbon Nano-Tube (CNT) or Al-Ti co-doped zinc oxide.

[0016] Correspondingly, there is provided an In-Cell
capacitive touch display device, including the In-Cell capacitive touch panel described above.

Correspondingly, there is provided a method for manufacturing the In-Cell capacitive touch panel described above, including:

preparing a substrate; and

sequentially forming a black matrix, a transparent conductive layer and color resistors on the substrate, where the black matrix is provided with a plurality of light-permeable regions, the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer are arranged above the light-impermeable regions of the black matrix and/or below the color resistors of the same color.

In the In-Cell capacitive touch panel, the display device and the method for manufacturing the In-Cell capacitive touch panel provided in the present invention, the light transmittance uniformity is improved in that the slots of the transparent conductive layer are hidden above the light-impermeable regions of the black matrix and/or below the color resistors of the same color, thereby avoiding the display of the pattern of the slots and improving the display effect, with comparison with the prior art.

Description of Drawings

In the In-Cell capacitive touch panel, the display device and the method for manufacturing the In-Cell capacitive touch panel provided in the present invention, light transmittance uniformity is improved in that the slots of the transparent conductive layer are hidden above the light-impermeable regions of the black matrix and/or below the color resistors of the same color, thereby avoiding the display of the pattern of the slots and improving the display effect, with comparison with the prior art.

Detailed description of the Preferred Embodiments

The embodiments of the present invention are further described in conjunction with the accompanying drawings so as to make the objects and characteristics of the present invention more apparent, but various variations may be made to the present invention and the invention is not intended to be limited to the present invention.

A First Embodiment

As shown in Figure 4, the present embodiment provides an In-Cell capacitive touch panel, including: a substrate 20; and a black matrix 21, a transparent conductive layer 22 and color resistors 23 sequentially formed on the substrate 20, where the black matrix 21 is provided with a plurality of light-permeable regions, the color resistors 23 cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer 22 are arranged above light-impermeable regions of the black matrix.

In the present embodiment, there are three kinds of color resistors, in particular, red (R) color resistors, Green (G) color resistors and blue (B) color resistors. The R, G and B color resistors are arranged alternately in a row direction (i.e. a horizontal direction), and the color resistors of the same color such as R color resistors, G color resistors or B color resistors are arranged in a column direction (i.e. a vertical direction). The light-impermeable regions of the black matrix can be described as a horizontal black matrix and a vertical black matrix. Preferably, a line width of the vertical black matrix (i.e. a line width of the light-impermeable region of the black matrix in the vertical direction) ranges from 7.0µm to 8.0µm; and a line width of the horizontal black matrix (i.e. a line width of the light-impermeable region of the black matrix in the horizontal direction) ranges from 24.5µm to 26.5µm.

In the present embodiment, the transparent conductive layer 22 is made of Indium Tin Oxide (ITO), Indium Zinc Oxide (IZO), Carbon Nano-Tube (CNT) or Al-Ti co-doped zinc oxide, the slots of the transparent conductive layer 22 are arranged at the light-impermeable regions of the black matrix between two rows of color resistors, and a line width of the slot of the transparent conductive layer, i.e. the line width of the horizontal slot, can be designed to be 10% to 90% of the line width of the horizontal black matrix, or be ranged from 3µm to 22µm. In the transparent conductive layer 22 of the present embodiment, a plurality of horizontal conductive lines in parallel are formed by the horizontal slots as sensing lines of a touch screen.

Preferably, the In-Cell capacitive touch panel further includes: a metal grid arranged between the black matrix 21 and the transparent conductive layer 22 (not shown); a first insulation layer (not shown) and a metal bridge (not shown) arranged between the transparent conductive layer 22 and the color resistors 23, and a second insulation layer (not shown) and a photolithography spacer (not shown) sequentially formed on the color re-
The present embodiment further provides an In-Cell capacitive touch display device, including the In-Cell capacitive touch panel described above.

The present embodiment further provides a method for manufacturing the In-Cell capacitive touch panel described above, including:

- preparing a substrate; and
- sequentially forming a black matrix, a transparent conductive layer and color resistors on the substrate, where the black matrix is provided with a plurality of light-permeable regions, the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer are horizontally arranged above the light-impermeable regions of the black matrix.

In the In-Cell capacitive touch panel, the In-Cell capacitive touch display device and the method for manufacturing the In-Cell capacitive touch panel provided in the present embodiment, the non-uniformity of the light transmittance (which is caused when the slots of the transparent conductive layer are arranged at different light-permeable regions) is avoided in that the slots of the transparent conductive layer are hidden above the light-impermeable regions of the black matrix, thereby avoiding the display of the pattern of the slots and improving the display effect.

A Second Embodiment

As shown in Figure 5, the present embodiment provides an In-Cell capacitive touch panel, including: a substrate; and a black matrix 21, a transparent conductive layer 22 and color resistors 23 sequentially formed on the substrate 20, where the black matrix 21 is provided with a plurality of light-permeable regions, the color resistors 23 cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer 22 are arranged above light-impermeable regions of the black matrix.

In the present embodiment, there are three kinds of color resistors, in particular R color resistors, G color resistors and B color resistors. The R, G and B color resistors are arranged alternately in a row direction (i.e. a horizontal direction), and the color resistors of the same color such as R color resistors, G color resistors or B color resistors are arranged in a column direction (i.e. a vertical direction).

The light-impermeable regions of the black matrix can be described as a horizontal black matrix and a vertical black matrix. Preferably, a line width of the vertical black matrix (i.e. a line width of the light-impermeable region of the black matrix in the vertical direction) ranges from 7.0μm to 8.0μm; and a line width of the horizontal black matrix (i.e. a line width of the light-impermeable region of the black matrix in the horizontal direction) ranges from 24.5μm to 26.5μm.

In the present embodiment, the transparent conductive layer 22 is made of Indium Tin Oxide (ITO), Indium Zinc Oxide (IZO), Carbon Nano-Tube (CNT) Al-Ti co-doped zinc oxide, the slots of the transparent conductive layer 22 are arranged at the light-impermeable regions of the black matrix between two columns of color resistors (i.e. arranged on the vertical black matrix), the line width of the slot of the transparent conductive layer, i.e. the line width of the vertical slot ranges from 2μm to 6μm. In the transparent conductive layer 22 of the present embodiment, a plurality of vertical conductive lines in parallel are formed by the vertical slots as driving lines of a touch screen.

Preferably, the In-Cell capacitive touch panel further includes: a metal grid arranged between the black matrix 21 and the transparent conductive layer 22; a first insulation layer and a metal bridge arranged between the transparent conductive layer 22 and the color resistors 23, and a second insulation layer and a photolithography spacer sequentially formed on the color resistors 23.

In the In-Cell capacitive touch display device, including the In-Cell capacitive touch panel described above.

The present embodiment further provides a method for manufacturing the In-Cell capacitive touch panel described above, including:

- preparing a substrate; and
- sequentially forming a black matrix, a transparent conductive layer and color resistors on the substrate, where the black matrix is provided with a plurality of light-permeable regions, the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, and the slots of the transparent conductive layer are vertically arranged above the light-impermeable regions of the black matrix.

In the In-Cell capacitive touch panel, the In-Cell capacitive touch display device and the method for manufacturing the In-Cell capacitive touch panel provided in the present embodiment, the non-uniformity of the light transmittance which is caused when the slots are arranged at different light-permeable regions is avoided in that the slots of the transparent conductive layer are hidden above the vertical light-impermeable regions of the black matrix, thereby avoiding the display of the pattern of the slots and improving the display effect. As compared with other embodiments, performances of the panel other than the display effect are less affected by arrangement of the slots due to the less distance between two adjacent color resistors in the horizontal direction, but it is more demanding for the manufacturing process accuracy.
A Third Embodiment

[0036] As shown in Figure 6, the present embodiment provides an In-Cell capacitive touch panel, including: a substrate 20; and a black matrix 21, a transparent conductive layer 22 and color resistors 23 sequentially formed on the substrate 20, where the black matrix 21 is provided with a plurality of light-permeable regions, the color resistors 23 cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer 22 are arranged below the color resistors of the same color, that is, below the column of the color resistors of the same color.

[0037] In the present embodiment, there are three kinds of color resistors, in particular R color resistors, G color resistors and B color resistors. The R, G and B color resistors are arranged alternately in a row direction (i.e. a horizontal direction), and the color resistors of the same color such as R color resistors, G color resistors or B color resistors are arranged in a column direction (i.e. a vertical direction).

[0038] The light-impermeable region of the black matrix can be described as a horizontal black matrix and a vertical black matrix. Preferably, a line width of the vertical black matrix (i.e. a line width of the light-impermeable region of the black matrix in the vertical direction) ranges from 7.0\( \mu \)m to 8.0\( \mu \)m; a line width of the horizontal black matrix (i.e. a line width of the light-impermeable region of the black matrix in the horizontal direction) ranges from 24.5\( \mu \)m to 26.5\( \mu \)m.

[0039] In the present embodiment, the transparent conductive layer 22 is made of Indium Tin Oxide (ITO), Indium Zinc Oxide (IZO), Carbon Nano-Tube (CNT) or Al-Ti co-doped zinc oxide, the slots of the transparent conductive layer 22 are arranged on the columns of the color resistors of the same color, the line width of the slot of the transparent conductive layer (e.g. the line width of the vertical slot), i.e. the width of the color resistor in the horizontal direction, ranges from the size of a pixel subtracted by the width of the light-impermeable region of the black matrix in the vertical direction to the size of the pixel plus the line width of the light-impermeable region of the black matrix in the vertical direction, for example, from 33.5\( \mu \)m to 38.5\( \mu \)m. In the transparent conductive layer 22 of the present embodiment, a plurality of vertical conductive lines in parallel are formed by the vertical slots as driving lines of a touch screen.

[0040] Preferably, the In-Cell capacitive touch panel further includes: a metal grid arranged between the black matrices 21 and the transparent conductive layer 22, a first insulation layer and a metal bridge arranged between the transparent conductive layer 22 and the color resistors 23, and a second insulation layer and a photolithography spacer sequentially formed on the color resistors 23.

[0041] The present embodiment further provides an In-Cell capacitive touch display device, including the In-Cell capacitive touch panel described above.

[0042] The present embodiment further provides a method for manufacturing the In-Cell capacitive touch panel described above, including:

preparing a substrate; and
sequentially forming a black matrix, a transparent conductive layer and color resistors on the substrate, where the black matrix is provided with a plurality of light-permeable regions, the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, and the slots of the transparent conductive layer are vertically arranged below the color resistors of the same color.

A Fourth Embodiment

[0043] In the In-Cell capacitive touch panel, the In-Cell capacitive touch display device and the method for manufacturing the In-Cell capacitive touch panel provided in the present embodiment, the problem of displaying the pattern of the slots caused by the non-uniformity of the light transmittance is avoided and the slots are hidden by arranging the slots of the transparent conductive layer below the color resistors of the same color, thereby improving the display effect.

[0044] As shown in Figure 7, the present embodiment provides an In-Cell capacitive touch panel, including: a substrate 20; and a black matrix 21, a transparent conductive layer 22 and color resistors 23 sequentially formed on the substrate 20, where the black matrix 21 is provided with a plurality of light-permeable regions, the color resistors 23 cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer 22 are arranged above the light-impermeable regions and below the color resistors of the same color (i.e. below the columns of the color resistors of the same color) respectively according to the designed pattern of ITO.

[0045] In the present embodiment, there are three kinds of color resistors, in particular R color resistors, G color resistors and B color resistors. The R, G and B color resistors are arranged alternately in a row direction (i.e. a horizontal direction), and the color resistors of the same color such as R color resistors, G color resistors or B color resistors are arranged in a column direction (i.e. a vertical direction).

[0046] A line width of the vertical black matrix (i.e. a line width of light-impermeable region of the black matrix in the vertical direction) ranges from 7.0\( \mu \)m to 8.0\( \mu \)m; and a line width of the horizontal black matrix (i.e. a line width of light-impermeable region of the black matrix in the horizontal direction) ranges from 24.5\( \mu \)m to 26.5\( \mu \)m.

[0047] In the present embodiment, the transparent conductive layer 22 is made of Indium Tin Oxide (ITO), Indium Zinc Oxide (IZO), a Carbon Nano-Tube (CNT) or Al-Ti co-doped zinc oxide, and the slots of the transparent...
conductive layer 22 are arranged above the light-impermeable regions of the black matrix and on the columns of the color resistors of the same color, that is, the slots of the transparent conductive layer include horizontal slots and vertical slots, and the slots of the present embodiment may be essentially regarded as the combination of the slots in the first embodiment, the second embodiment and the third embodiment.

[0048] Preferably, the In-Cell capacitive touch panel further includes: a metal grid arranged between the black matrix 21 and the transparent conductive layer 22; a first insulation layer and a metal bridge arranged between the transparent conductive layer 22 and the color resistors 23; and a second insulation layer and a photolithography spacer sequentially formed on the color resistors 23.

[0049] The present embodiment further provides an In-Cell capacitive touch display device, including the In-Cell capacitive touch panel described above.

[0050] The present embodiment further provides a method for manufacturing the In-Cell capacitive touch panel described above, including:

[0051] preparing a substrate; and

[0052] sequentially forming a black matrix, a transparent conductive layer and color resistors on the substrate, where the black matrix is provided with a plurality of light-permeable regions, the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, and the slots of the transparent conductive layer are vertically arranged above the light-impermeable regions of the black matrix and below the color resistors of the same color.

[0053] In the In-Cell capacitive touch panel, the In-Cell capacitive touch display device and the method for manufacturing the In-Cell capacitive touch panel provided in the present embodiment, the problem of displaying the pattern of the slots caused by the non-uniformity of the light transmittance is avoided and the slots are hidden by arranging the slots of the transparent conductive layer above the light-impermeable regions of the black matrix and below the color resistors of the same color, thereby improving the display effect.

[0054] Obviously, various modifications and variations may be made by those skilled in the art without departing from the principle and scope of the invention. Thus, all these modifications and variations are intended to be included in this invention if they fall within the scope of the claims of the invention and their equivalents.

Claims

1. An In-Cell capacitive touch panel, comprising:

   a substrate; and a black matrix, a transparent conductive layer and color resistors sequentially formed on the substrate, wherein the black matrix is provided with a plurality of light-permeable regions, the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer are arranged above light-impermeable regions of the black matrix and/or below the color resistors of the same color.

2. The In-Cell capacitive touch panel of claim 1, wherein, the arrangement rule is that R, G, and B color resistors are alternatively arranged in a horizontal direction and the color resistors of the same color are arranged in a vertical direction.

3. The In-Cell capacitive touch panel of claim 2, wherein, a line width of the light-impermeable region of the black matrix in the vertical direction ranges from 7.0 μm to 8.0 μm.

4. The In-Cell capacitive touch panel of claim 3, wherein, a line width of the light-impermeable region of the black matrix in the horizontal direction ranges from 24.5 μm to 26.5 μm.

5. The In-Cell capacitive touch panel of claim 2, wherein, horizontal slots of the transparent conductive layer are arranged above the light-impermeable regions of the black matrix, and vertical slots of the transparent conductive layer are arranged below the color resistors of the same color.

6. The In-Cell capacitive touch panel according to any one of claims 2 to 5, wherein, a line width of the horizontal slot of the transparent conductive layer is 10% to 90% of the line width of the light-impermeable region of the black matrix in the horizontal direction.

7. The In-Cell capacitive touch panel according to any one of claims 2 to 5, wherein, the line width of the horizontal slot of the transparent conductive layer ranges from 3 μm to 22 μm.

8. The In-Cell capacitive touch panel according to any one of claims 2 to 5, wherein, the line width of the vertical slot of the transparent conductive layer ranges from the size of a pixel subtracted by the width of the light-impermeable region of the black matrix in the vertical direction to the size of the pixel plus the line width of the light-impermeable region of the black matrix in the vertical direction.

9. The In-Cell capacitive touch panel according to any one of claims 2 to 5, wherein, the line width of the vertical slot of the transparent conductive layer ranges from 33.5 μm to 38.5 μm.

10. The In-Cell capacitive touch panel of claim 1, further comprising a metal grid arranged between the black matrix and the transparent conductive layer.
11. The In-Cell capacitive touch panel of claim 1 or claim 10, further comprising a first insulation layer and a metal bridge arranged between the transparent conductive layer and the color resistors.

12. The In-Cell capacitive touch panel of claim 1, further comprising a second insulation layer and a photolithography spacer sequentially formed on the color resistors.

13. The In-Cell capacitive touch panel of claim 1, wherein, the transparent conductive layer is made of Indium Tin Oxide (ITO), Indium Zinc Oxide (IZO), Carbon Nano-Tube (CNT), or Al-Ti co-doped zinc oxide.

14. An In-Cell capacitive touch display device, comprising the In-Cell capacitive touch panel according to any one of claims 1 to 13.

15. A method for manufacturing the In-Cell capacitive touch panel according to any one of claims 1 to 13, comprising:

preparing a substrate; and sequentially forming a black matrix, a transparent conductive layer and color resistors on the substrate, wherein the black matrix is provided with a plurality of light-permeable regions, the color resistors cover the light-permeable regions of the black matrix according to a certain arrangement rule, and slots of the transparent conductive layer are arranged above the light-impermeable regions of the black matrix and/or below the color resistors of the same color.
FIG. 5

FIG. 6
**INTERNATIONAL SEARCH REPORT**

**A. CLASSIFICATION OF SUBJECT MATTER**

See the extra sheet

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

- IPC: G02F 1/01; G06F 3/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic database consulted during the international search (name of data base and, where practicable, search terms used)

- CNPAT, CNKI, CNTXT, WPI, EPDOC: black, matrix, shading, slot, touch+, panel, screen, black w matrix, brn, color, colour, filter, red, green, blue, R, G, B, slit, same, distance, space, interval, hide, hiding

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of document, with indication, where appropriate, of the relevant passages</th>
<th>Relevant to claim No.</th>
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<tbody>
<tr>
<td>X</td>
<td>CN 102207614 A (LG DISPLAY CO., LTD.), 05 October 2011 (05.10.2011), description, paragraphs 0030-0044, claims 1-12, and figures 1 and 4</td>
<td>1-4, 6, 7, 9-15</td>
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<tr>
<td>X</td>
<td>CN 102466907 A (SAMSUNG MOBILE DISPLAY CO., LTD.), 23 May 2012 (23.05.2012), description, paragraphs 0044-0098 and 0124, and figures 1-4 and 7</td>
<td>1-4, 6, 7, 9-15</td>
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<td>A</td>
<td>US 2012062486 A1 (RHO), 15 March 2012 (15.03.2012), the whole document</td>
<td>1-7, 9-15</td>
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- **Further documents are listed in the continuation of Box C.**

- **See patent family annex.**

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<th>Special categories of cited documents:</th>
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<tr>
<td>&quot;A&quot; document defining the general state of the art which is not considered to be of particular relevance</td>
</tr>
<tr>
<td>&quot;E&quot; earlier application or patent but published on or after the international filing date</td>
</tr>
<tr>
<td>&quot;L&quot; document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</td>
</tr>
<tr>
<td>&quot;O&quot; document referring to an oral disclosure, use, exhibition or other means</td>
</tr>
<tr>
<td>&quot;P&quot; document published prior to the international filing date but later than the priority date claimed</td>
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</table>

- **"T"** later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

- **"X"** document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

- **"Y"** document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

- **"R"** document member of the same patent family

- **Date of the actual completion of the international search**
  15 June 2013 (15.06.2013)

- **Date of mailing of the international search report**
  04 July 2013 (04.07.2013)

**Name and mailing address of the ISA/CN:**

State Intellectual Property Office of the P. R. China
No. 6, Xitucheng Road, Jimei jiao
Haidian District, Beijing 100088, China
Facsimile No.: (86-10) 62019451

**Authorized officer**

ZHENG, Jiaqing

**Telephone No.**

(86-10) 62411684

Form PCT/ISA/210 (second sheet) (July 2009)
**INTERNATIONAL SEARCH REPORT**

**Box No. II  Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)**

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1.☐  **Claims Nos.**
   because they relate to subject matter not required to be searched by this Authority, namely:

2.☒  **Claim No. 8**
   because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
   the feature that “the line width of said longitudinal reticule on the transparent conducting layer is the difference between the value obtained by subtracting the line width of said black matrix opaque area in the longitudinal direction from a pixel size and the value obtained by adding the line width of said black matrix opaque area in the longitudinal direction to a pixel size” of claim 8 is unclear; and since the range of the longitudinal reticule line width cannot be understood and determined clearly through the above expression, claim 8 is unclear. (PCT Article 6)

3.☐  **Claims Nos.**
   because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

**Box No. III  Observations where unity of invention is lacking (Continuation of item 3 of first sheet)**

This International Searching Authority found multiple inventions in this international application, as follows:

1.☐  As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.

2.☐  As all searchable claims could be searched without effort justifying additional fees, this Authority did not invite payment of additional fees.

3.☐  As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:

4.☐  No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

**Remark on protest**

☐  The additional search fees were accompanied by the applicant’s protest and, where applicable, the payment of a protest fee.

☐  The additional search fees were accompanied by the applicant’s protest but the applicable protest fee was not paid within the time limit specified in the invitation.

☐  No protest accompanied the payment of additional search fees.
## INTERNATIONAL SEARCH REPORT
Information on patent family members

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<th>Patent Documents referred in the Report</th>
<th>Publication Date</th>
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INTERNATIONAL SEARCH REPORT

CONTINUATION OF SECOND SHEET

A. CLASSIFICATION OF SUBJECT MATTER

G02F 1/133 (2006.01) i
G06F 3/044 (2006.01) i