**Aminovinylsilane for cvd and ald siO2 films**

This invention relates to methods to form silicon dioxide films that have extremely low wet etch rates in HF solution using a thermal CVD process, ALD process, or cyclic CVD process in which the silicon precursor is selected from one of:

- \(R_1^nR_2^mSi(NR_3R_4)^{4-n-m}\); and,
- a cyclic silazane of \((R_1^1R_2^2SiNR_3)^p\),

where \(R_1^1\) is an alkenyl or aromatic, such as vinyl, allyl, and phenyl; \(R_2^2\) is selected from \(H\), alkyl with \(C_1-C_{10}\) linear, branched, or cyclic, an alkenyl with \(C_2-C_{10}\) linear, branched, or cyclic, and aromatic; \(R_3^3\) and \(R_4^4\) are selected from \(H\), alkyl with \(C_1-C_{10}\) linear, branched, or cyclic, an alkenyl with \(C_2-C_{10}\) linear, branched, or cyclic, and aromatic, or are joined such that \(NR_3R_4^4\) is a cyclic amine; \(n=1-3\), \(m=0-2\), \(p=3-4\).
BACKGROUND OF THE INVENTION

[0001] Thin films of silicon dioxide, silicon nitride, and mixtures thereof are some of the most commonly used materials in semiconductor manufacturing due to their excellent dielectric properties. In the manufacturing of silicon based semiconductor devices, these materials can be used as gate insulator, diffusion masks, side wall spacers, hard mask, anti-reflection coating, passivation and encapsulation, etc. Silicon-based films are also becoming increasingly important for passivation of other compound semiconductor devices.

[0002] When silicon-based films are used in conjunction with wet etch process, an important and routine production process for the fabrication of silicon integrated circuits, the wet etch rate of silicon dioxide films are critical to many applications. In some cases (e.g., when the silicon dioxide is used side wall), the etch rate in HF solution needs to extremely slow since a too fast and aggressive action on the material would make it difficult to control the undercut and the line-width. A slower, controllable etch rate is desirable for a better manufacturing process, supporting higher yield of semiconductor devices. In some other cases where silicon-based films are used as etch stops, hard masks, or passivation layers, it is desirable for the material to be extremely resistant to wet etching.

[0003] Existing approaches of forming silicon-based films that have low etch rate in HF solution are

(1) depositing the films at higher temperatures to reduce the defects such as porosity or hydrogen concentration in the film, or
(2) adding other precursors to the deposition process in addition to silicon or nitrogen during the deposition process to bring in additional elements to modify film properties.

[0004] Since higher temperatures may not always be desirable, and the use of multiple precursors can add complexity to the process, alternatives to controlling film properties are desired.


BRIEF SUMMARY OF THE INVENTION

[0006] In a first aspect, the present invention is a method of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, wherein the method is a low pressure, thermal chemical vapor deposition method, atomic layer deposition method or cyclic chemical vapor deposition method, and comprises:

a. delivering a first precursor that provides a source for silicon to a low pressure, thermal chemical vapor deposition, an atomic layer deposition or a cyclic chemical vapor deposition reactor, wherein the first precursor is selected from the group consisting of: R1nR2mSi(NR3R4)4-n-m, and a cyclic silazane of the formula (R1R2SiNR3)p; where R1 is an alkenyl or an aromatic group, preferably a C2-C10 alkenyl or aromatic group; R2 is H, a C1-C10, linear, branched, or cyclic alkyl, a C2-C10 linear, branched, or cyclic alkenyl, or an aromatic group; R3 and R4 are each selected from H, a C1-C10, linear, branched, or cyclic alkyl, a C2-C10 linear, branched, or cyclic alkenyl, and an aromatic group, or are joined such that NR3R4 is a cyclic amine; and n=1 to 3, m=0 to 2, and p=3 or 4; and
b. delivering a second precursor that provides a source for oxygen to the reactor.

[0007] In a second aspect, the present invention is a low pressure, thermal chemical vapor deposition method of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, the method comprising:

a. delivering a first precursor that provides a source for silicon to a low pressure, thermal chemical vapor deposition reactor, wherein the first precursor is selected from the group consisting of: R1nR2mSi(NR3R4)4-n-m, and a cyclic silazane of the formula (R1R2SiNR3)p; where R1 is an alkenyl or an aromatic group, preferably a C2-C10 alkenyl an aromatic group, such as for example vinyl, allyl, or phenyl; R2 is H, a C1-C10, linear, branched, or cyclic alkyl, a C2-C10 linear, branched, or cyclic alkenyl, or an aromatic group; R3 and R4 are each selected from H, a C1-C10, linear, branched, or cyclic alkyl, a C2-C10 linear, branched, or cyclic alkenyl, or an aromatic group, or are joined such that NR3R4 is a cyclic amine; and n=1 to 3, m=0 to 2, and p=3 or 4; and
b. delivering a second precursor that provides a source for oxygen to the reactor;

[0008] In a third aspect, the present invention is an atomic layer deposition method of forming a silicon dioxide film
In a fourth aspect, the present invention is a cyclic chemical vapor deposition method of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, comprising:

a. delivering a first precursor that provides a source for silicon to a cyclic chemical vapor deposition reactor, wherein the first precursor is selected from the group consisting of: \( R_1^{n} R_2^{m} Si(NR_3R_4)^{4-n-m} \), and a cyclic silazane of the formula \( (R_1R_2SiNR_3)^p \); where \( R_1 \) is an alkenyl or an aromatic group, preferably a \( C_2-C_{10} \) alkenyl or aromatic group, such as for example vinyl, allyl, or phenyl; \( R_2 \) is \( H \), a \( C_1-C_{10} \) linear, branched, or cyclic alky, a \( C_2-C_{10} \) linear, branched, or cyclic alkenyl, or an aromatic group; \( R_3 \) and \( R_4 \) are each selected from \( H \), a \( C_1-C_{10} \) linear, branched, or cyclic alkenyl, or cyclic alkenyl, and an aromatic group, or are joined such that \( NR_3R_4 \) is a cyclic amine; and \( n=1 \) to 3, \( m=0 \) to 2, and \( p=3 \) or 4;

b. purging the reactor with an inert gas, such as for example Ar or \( \text{N}_2 \);

c. delivering a second precursor that provides a source for oxygen to the reactor;

d. purging the reactor with an inert gas, such as for example Ar or \( \text{N}_2 \);

e. repeating steps (a) to (d) until a desired thickness of the film is achieved.

Aspects of the invention also include the following aspects numbered #1 to #12:

#1. A low pressure, thermal chemical vapor deposition method of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, comprising:

a. delivering a first precursor that provides a source for silicon to a low pressure, thermal chemical vapor deposition reactor in which the first precursor is selected from the group consisting of: \( R_1^{n} R_2^{m} Si(NR_3R_4)^{4-n-m} \), and a cyclic silazane of the formula \( (R_1R_2SiNR_3)^p \); where \( R_1 \) is an alkenyl or an aromatic, preferably a \( C_2-C_{10} \) alkenyl or aromatic group, such as for example vinyl, allyl, or phenyl; \( R_2 \) is \( H \), a \( C_1-C_{10} \) linear, branched, or cyclic alky, a \( C_2-C_{10} \) linear, branched, or cyclic alkenyl, or an aromatic group; \( R_3 \) and \( R_4 \) are each selected from \( H \), a \( C_1-C_{10} \) linear, branched, or cyclic alkenyl, or cyclic alkenyl, and an aromatic group, or are joined such that \( NR_3R_4 \) is a cyclic amine, and \( n=1 \) to 3, \( m=0 \) to 2, and \( p=3 \) or 4;
aromatic; R², R³, and R⁴ are selected from H, alkyl with C₁-C₁₀, linear, branched, or cyclic, an alkenyl with C₂-C₁₀ linear, branched, or cyclic, and aromatic; and n=1-3, m=0-2, p=3-4;

b. delivering a second precursor that provide a source for oxygen to the reactor;
c. reacting the first and second precursors at temperature between 400°C to 700°C under a pressure of 100mT to 1T.

#2. A method according to #1 in which R¹ is selected from the group consisting of vinyl, allyl, and phenyl.

#3. A method according to #1 in which the first precursor is Bis(isopropylamino)vinylmethylsilane.

#4. A method according to #1 in which the oxygen precursor is selected from the group consisting of oxygen, ozone and mixtures thereof.

#5. An atomic layer deposition method of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, comprising:

a. delivering a first precursor that provides a source for silicon to an atomic layer deposition reactor in which the first precursor is selected from the group consisting of: R¹ᵣR²ᵣₘSi(NR₃R₄)₄₋ᵣ₋ₘ, and a cyclic silazane of (R¹R²SiNR₃)ₚ; where R¹ is an alkenyl or an aromatic, preferably a C₂-C₁₀ alkenyl or an aromatic; R², R₃, and R₄ are selected from H, alkyl with C₁-C₁₀, linear, branched, or cyclic, an alkenyl with C₂-C₁₀ linear, branched, or cyclic, and aromatic; and n=1-3, m=0-2, p=3-4;
b. purging the reactor with an inert gas;
c. delivering a second precursor that provide a source for oxygen to the reactor;
d. purging the reactor with an inert gas;
e. repeating the steps between (a) and (d) until a desired thickness of the films is achieved.

#6. A method according to #1 in which R¹ is selected from the group consisting of vinyl, allyl, and phenyl.

#7. A method according to #1 in which the first precursor is Bis(isopropylamino)vinylmethylsilane.

#8. A method according to #5 in which the oxygen precursor is selected from the group consisting of oxygen, ozone and mixtures thereof.

#9. A cyclic chemical vapor deposition method of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, comprising:

a. delivering a first precursor that provides a source for silicon to a cyclic chemical vapor deposition reactor in which the first precursor is selected from the group consisting of: R¹ᵣR²ᵣₘSi(NR₃R₄)₄₋ᵣ₋ₘ, and a cyclic silazane of (R¹R²SiNR₃)ₚ; where R¹ is an alkenyl or an aromatic, preferably a C₂-C₁₀ alkenyl or an aromatic; R², R₃, and R₄ are selected from H, alkyl with C₁-C₁₀, linear, branched, or cyclic, an alkenyl with C₂-C₁₀ linear, branched, or cyclic, and aromatic; and n=1-3, m=0-2, p=3-4;
b. purging the reactor with an inert gas for 0.1 to 1 seconds;
c. delivering a second precursor that provides a source of oxygen to the reactor;
d. purging the reactor with an inert gas for 0.1-1 seconds;
e. repeating the steps between (a) and (d) until a desired thickness of the films is achieved.

#10. A method according to #9 in which R¹ is selected from the group consisting of vinyl, allyl, and phenyl.

#11. A method according to #9 in which the first precursor is Bis(isopropylamino)vinylmethylsilane.

#12. A method according to #9 in which the oxygen precursor is selected from the group consisting of oxygen, ozone and mixtures thereof.

BRIEF DESCRIPTION OF SEVERAL VIEWS OF THE DRAWINGS

FIGURE 1 is a schematic illustration of a cross-section of a CVD reactor.
FIGURE 2 is a schematic illustration of a cross-section of a ALD reactor.

FIGURE 3 is an X-ray Photoelectron Spectroscopy (XPS) of a low etch SiO₂ film.

FIGURE 4 is a graph showing a depth profile of a bulk composition deposited in accordance with the present invention.

DETAILED DESCRIPTION OF THE INVENTION

[0019] In one embodiment, this invention provides a method of forming silicon-based films that have extremely low wet etch rate in HF solution using a thermal CVD process. The said method comprises:

a) Placing a substrate in a concealed reactor that is at a temperature of 400-700°C, is purged with Ar or N₂ gas and maintained at a pressure below 1 Torr (130 Pa);

b) Feeding the reactor with a silicon precursor that is delivered using a direct liquid injector and has one of the following structures:

\[ R_{1}^{n}R_{2}^{m}Si(NR_{3}R_{4})_{4-n-m}; \]

and,

a cyclic silazane of \((R_{1}R_{2}SiNR_{3})_{p}\); such as:

\[
\begin{align*}
\text{R}^{1} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{1} \\
\text{R}^{2} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{2} \\
\text{R}^{3} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{3} \\
\text{R}^{4} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{4}
\end{align*}
\]

where \(R_{1}\) is a C₂-C₁₀ alkenyl or an aromatic, such as vinyl, allyl, and phenyl; \(R_{2}\) is selected from H, alkyl with C₁-C₁₀ linear, branched, or cyclic, an alkenyl with C₂-C₁₀ linear, branched, or cyclic, and aromatic; \(R_{3}\) and \(R_{4}\) are selected from H, alkyl with C₁-C₁₀ linear, branched, or cyclic, an alkenyl with C₂-C₁₀ linear, branched, or cyclic, and aromatic, or are joined such that NR₃NR₄ is a cyclic amine. \(n=1-3, \ m=0-2; \ p=3-4; \)

c) At the same time, feeding the reactor with oxygen precursor such as pure oxygen or ozone;

d) A pressure of 100 mTorr to 600 mTorr (13 to 80 Pa) is maintained during the deposition process.

[0020] In another embodiment, this invention provides a method of forming silicon dioxide films that have extremely low wet etch rate in HF solution using an atomic layer deposition process. The said method comprises:

a) Placing a substrate in a concealed reactor that is at a temperature of 100-600°C, is purged with Ar or N₂ gas and maintained at a pressure below 1 Torr (130 Pa);

b) Feeding the reactor with a silicon precursor that is delivered using a direct liquid injector and has one of the following structures:

\[ R_{1}^{n}R_{2}^{m}Si(NR_{3}R_{4})_{4-n-m}; \]

and,

a cyclic silazane of \((R_{1}R_{2}SiNR_{3})_{p}\); such as:

\[
\begin{align*}
\text{R}^{1} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{1} \\
\text{R}^{2} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{2} \\
\text{R}^{3} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{3} \\
\text{R}^{4} & \quad \text{N} & \quad \text{Si} & \quad \text{N} & \quad \text{R}^{4}
\end{align*}
\]

Where \(R_{1}\) is a C₂-C₁₀ alkenyl or an aromatic, such as vinyl, allyl, and phenyl; \(R_{2}\) is selected from H, alkyl with C₁-C₁₀.
linear, branched, or cyclic, an alkenyl with C₂⁻C₁₀ linear, branched, or cyclic, and aromatic; R³ and R⁴ are selected from H, alkyl with C₁⁻C₁₀, linear, branched, or cyclic, an alkenyl with C₂⁻C₁₀ linear, branched, or cyclic, and aromatic, or are joined such that NR₃R₄ is a cyclic amine. n=1-3, m=0-2; p=3-4;

c) The dose of the silicon precursor is just enough to form a monolayer of the precursor molecule on the surface of the substrate. The excess of the precursor is removed from the reactor;

d) After the reactor is purged with Ar or N₂ again, feeding the reactor with oxygen precursor such as pure oxygen or ozone; Again, the dose of the oxygen precursor is just enough to completely react with the monolayer silicon precursor on the substrate formed during the last step, and following this dose the reactor is again purged with Ar or N₂;

e) Repeating the steps b) to d) until a desired thick of the film is reached.

[0021] In another embodiment, this invention provides a method of forming silicon dioxide films that have extremely low wet etch rate in HF solution using a cyclic chemical vapor deposition process. The said method comprises:

a) Placing a substrate in a concealed reactor that is at a temperature of 400-700°C, is purged with Ar or N₂ gas and maintained at a pressure below 1 Torr (130 Pa);

b) Feeding the reactor with a silicon precursor that is delivered using a direct liquid injector and has one of the following structures:

\[ R_1^nR_2^mSi(NR_3R_4)^{4-n-m}; \]

and, a cyclic silazane of \((R_1R_2SiNR_3)^p;\) such as:

\[ \text{where } R_1 \text{ is an alkenyl or an aromatic, such as vinyl, allyl, and phenyl; } R_2 \text{ selected from } H, \text{ alkyl with } C_1-C_{10}, \text{ linear, branched, or cyclic, an alkenyl with } C_2-C_{10} \text{ linear, branched, or cyclic, and aromatic; } R_3 \text{ and } R_4 \text{ are selected from } H, \text{ alkyl with } C_1-C_{10}, \text{ linear, branched, or cyclic, an alkenyl with } C_2-C_{10} \text{ linear, branched, or cyclic, and aromatic, or are joined such that NR}_3R_4 \text{ is a cyclic amine. } n=1-3, m=0-2; p=3-4; \]

c) The dose of the silicon precursor is adjusted (increased or reduced) to achieve a desired deposition rate of the film;

d) Feeding the reactor with oxygen precursor such as pure oxygen or ozone; Again, the dose of the oxygen precursor is adjusted (increased or reduced) to achieve a desired deposition rate of the film;

e) Purging the reactor with Ar or N₂;

f) Repeating steps b) to e) until a desired thickness of the film is reached.

[0022] The silicon precursor is preferably an amino vinylsilane precursor such as, but not limited to, Bis(isopropylamino)vinylmethylsilane (BIPAVNS), Bis(isopropylamino)divinylsilane (BIPADVS), Bis(isopropylamino)vinylsilane, Bis(isopropylamino)allylmethylsilane, Bis(isopropylamino)allylsilane, Bis(t-butylamino)vinylmethylsilane, Bis(t-butylamino)divinylsilane, Bis(t-butylamino)vinylsilane, Bis(t-butylamino)allylmethylsilane, Bis(t-butylamino)allylsilane, Bis(diethylamino)vinylmethylsilane, Bis(diethylamino)divinylsilane, Bis(diethylamino)vinylsilane, Bis(diethylamino)allylmethylsilane, Bis(diethylamino)allylsilane, Bis(dimethylamino)vinylmethylsilane, Bis(dimethylamino)divinylsilane, Bis(dimethylamino)vinylsilane, Bis(dimethylamino)allylmethylsilane, Bis(dimethylamino)allylsilane, Bis(methylethylamino)vinylmethylsilane, Bis(methylethylamino)divinylsilane, Bis(methylethylamino)vinylsilane, Bis(methylethylamino)allylmethylsilane, Bis(methylethylamino)allylsilane, Dipiperidinovinylmethylsilane, Dipiperidinodivinylsilane, Dipiperidinovinylsilane,
The CVD reactor 300 is heated by the surrounding heating element 201. All the wafers 203 are loaded on precursor is Bis(iso-propylamino)divinylsilane (BIPADVS). 1,3,5,7-tetraallylcyclotetrasilazane, 1,1,3,3,5,5-octaallylcyclotetrasilazane, 1,1,3,3,5,5-hexaallylcyclotrisilazane, 1,3,5-triallyl-1,3,5-trimethylcyclotetrasilazane, 1,3,5-triallyl-1,3,5-trimethylcyclotrisilazane, 1,1,3,3,5,5-hexavinylcyclotrisilazane, 1,3,5-triallyl-1,3,5-trimethylcyclotetrasilazane, 1,3,5-triallyl-1,3,5-trimethylcyclotrisilazane, 1,3,5,7-tetraallyl-1,3,5,7-tetramethylcyclotetrasilazane, 1,3,5,7-tetraallyl-1,3,5,7-tetramethylcyclotrisilazane, 1,3,5,7-tetraallylcyclotetrasilazane, 1,1,3,3,5,5,7,7-octavinylcyclotetrasilazane, 1,3,5,7-tetraallyl-1,3,5,7-tetramethylcyclotetrasilazane, 1,3,5,7-tetraallylcyclotetrasilazane, 1,1,3,3,5,5,7,7-octavinylcyclotetrasilazane.

The particular precursor used in tests is Bis(iso-propylamino)vinylmethyilsilane (BIPAVMS). Another similar precursor is Bis(iso-propylamino)divinylsilane (BIPADVS).

Before the film deposition process starts, the CVD tube is filled with inert gas (e.g. Ar or N\textsubscript{2}) through inlet 103 and then pumped using a vacuum pump or process pump 302 to a vacuum level below 1 mTorr (0.13 Pa) and exhausted through abatement 303. The CVD reactor is then filled with inert gas again and heated to a temperature at which the deposition is set to begin. Once the CVD reactor reaches the set temperature, the valve 103A is closed and valves 101A and 102A opens to introduce precursor vapor and reactive gas into the CVD reactor. The pressure of the CVD reactor is controlled by varying the opening of the throttle valve 301.

The reactive gas (e.g. O\textsubscript{2}) flows into the CVD reactor through inlet 102 and the flow rate is controlled by a mass flow controller (MFC). The silicon precursor is in liquid form and filled in a liquid container 101 D. High pressure He gas is used to help push the liquid into a vaporizer 101 B that heats the liquid and vaporizes the liquid. The flow the liquid precursor is controlled using a liquid flow controller (LFC). The flow rate of the precursor vapor can be controlled by LFC and the heating temperature of the vaporizer 101 B: for a given setting of LFC, the higher the temperature of the vaporizer, the bigger the flow rate of the precursor vapor; for a given temperature setting of the vaporizer, the higher the flow rate of the LCF, the bigger the flow rate of the precursor vapor. The valve 101A opens or stops the flow of the precursor vapor to the CVD reactor.

The pressure of the CVD reactor can be in the range of about 0.01 Torr to about 1 Torr (1.3 to 130 Pa). The flow rate of the reactive gas (e.g. O\textsubscript{2}) can be in the range of 5 sccm to 200 sccm. The deposition temperature is the same as the reactor wall temperature. It can be in the range of 500°C to 700°C.

The deposition time is pre-set for the process to yield films with a desired thickness. The deposition rate is dependent of many processing parameters, including: the deposition temperature, the flow rate of O\textsubscript{2}, the flow rate of carrier gas (He), the liquid mass flow of the Si precursor, the temperature of the vaporizer, and the pressure of the reactor. The vaporizer temperature can be in the range of 20°C to 150°C. At the vaporizer temperature of 55°C, the rate of the deposition of the SiO\textsubscript{2} can be in the range of 0.1 nm to 1 nm per minute. The rate can be controlled by varying the deposition temperature, the vaporizer temperature, the flow of the LFC, the flow rate of reactive O\textsubscript{2} gas and the pressure at the CVD reactor.

The method of forming uniform nitrogen free silicon dioxide films using an ALD process can be demonstrated using an ALD reactor illustrated in Figure 2. An ALD reactor A200 consists of a chamber wall A300 and substrate holder A202. Both of them can be heated separately at different temperatures. Wafers (or substrates) A203A are placed on A202 during the deposition process.

Before the film deposition process starts, the ALD reactor A200 is filled with inert gas (e.g. Ar or N\textsubscript{2}) through inlet A104 and valve A104A and them pumped using a vacuum pump or process pump A302 to a vacuum level below 1 mTorr (0.13 Pa) and exhausted to abatement A303. The ALD reactor A200 is then filled with inert gas again and the substrate holder A202 and the reactor wall are heated by heaters A201 to a temperature at which the deposition is set to begin. The Si precursor is delivered from a vapor draw A101 that is heated by an electric heater A101 E surrounding Si precursor container A101 D and maintained at a constant temperature during the deposition. The flow of the Si precursor is controlled through a high speed ALD valve A101A, mass flow controller A101 B and valve A101C.

The temperature is between 20°C to 100°C. Helium can be introduced simultaneous with the Si precursor
through line A103 and valve A103A. The oxygen precursor is delivered through a container A102D having heater A102E through a high speed ALD valve A102A controlling line A102 along with mass flow controller A102B and valve A102C.  

[0034] The sequence of the ALD process is as follows:

- Feed silicon precursor reactor A200 for 0.1-10 seconds by closing valve A301 and opening valve A101A for the same amount of time;
- Purge reactor A200 for 0.5-5 seconds by closing valve A101A and opening valve A301 and A104A for the same amount of time;
- Feed oxygen into reactor A200 for 0.1-10 seconds by closing valve A301 and opening valve A102A for the same amount of time;
- Purge reactor A200 for 0.5-5 seconds by closing valve A102A and opening valve A301 for the same amount of time;
- Repeat the above cycles for a number of times until a desired thickness of the film is achieved.

Cyclic CVD process

[0035] The cyclic CVD process of forming uniform nitrogen free silicon dioxide films can also be illustrated in Figure 2. The same ALD reactor can be used for the cyclic CVD process. The major difference in the cyclic CVD process to deposit uniform nitrogen free films is that the dosages of the silicon precursor and oxygen precursor can be much bigger than the ones used for ALD, and thus the deposition rate can be much higher than ALD. The deposition temperature, however, is 400-700°C, much higher than that used for ALD process.

[0036] The sequence of the cyclic CVD process is as follows:

- Feed silicon precursor reactor A200 for 5-20 seconds by closing valve A301 and opening valve A101A for the same amount of time;
- Purge reactor A200 for 0.1-1 seconds by closing valve A101A and opening valves A301 and A104A for the same amount of time;
- Feed oxygen to reactor A200 for 5-20 seconds by closing valve A301 and opening valve A102A for the same amount of time;
- Purge reactor A200 for 0.1-1 seconds by closing valve A102A and opening valves A301 and A104A for the same amount of time;
- Repeat the above cycles for a number of times until a desired thickness of the film is achieved.

Film thickness and optical property

[0037] The characterization of thickness and optical properties such as refractive index of the oxide films is performed using a FilmTek 2000SE ellipsometer. Spectroscopic reflection data at normal incidence (angle of incidence = 0°) are used for the data fitting. The range of wavelength of the light used is between 200 nm to 900 nm. Since the extinction coefficient (k) for SiO₂ is zero when the wavelength of the light is between 200 nm and 4000 nm and the dispersion of SiO₂ is well known, the data are only fit on the high frequency dielectric constant. The thickness and refractive index of the oxide film can be obtained by fitting the reflection data from the film to a pre-set physical model (e.g., the Lorentz Oscillator model). The RMSE (root of mean square error) is used to determine the goodness of the fitting and the value has to be less than 1 % for the results of the measurement to be considered reliable.

Chemical composition

[0038] The characterization of the chemical composition of the films is accomplished with X-ray Photoelectron Spectrometry (XPS). The X-ray Photoelectron Spectroscopy experiments are performed on a PHI 5000 VersaProbe Spectrometer equipped with Multiple Channels Plates (MCD) and a focused Al monochromatic X-ray source. The low resolution survey scan is performed at 117.4 eV Pass Energy, 1.000 eV/Step and a 50 msec/step dwell time. The high resolution multiplex scans are performed at 23.50 eV Pass Energy, 0.100 eV/Step and a 100 msec/step dwell time. The analysis area is 200 microns in diameter with a take-off angle of 45°. The data is collected using vendor supplied software. CasaXPS is used to work up the data using transmission function corrected Area Sensitivity Factors (ASF). All spectra are charge corrected to CHx=284.8 eV. The etch rate is calibrated against 203 nm SiO₂/Si and is approximately 120 Å/min.

Etch rate in diluted HF solution

[0039] The etch test is carried out at 1 wt% HF (in deionized (DI) water) solution. The films (deposited on Si wafers) are placed in HF solution for 30 seconds, followed by rinsing in DI wafer and drying before being measured again for
the loss of the material during the etch. Two thermal silicon oxide films with a known and consistent etch rate as references are loaded in the same cassette with the films to be characterized and etched at the same time. The films, along with reference thermal oxide films, are measured for their thickness at 9 different points across the film surface before and after etch using an ellipsometer and a method described above. The etch rate is then calculated as the thickness reduction divided by the time that the films are immersed into the HF solution.

Dielectric constants

[0040] Dielectric constants, k, are calculated from a C-V curve measured with a MDC 802B-150 Mercury Probe. It consists of a probe stage that holds the sample and forms electrical contacts on the film to be measured, a Keithley 236 source meter and HP4284A LCR meter for C-V measurement. Si wafers that have relatively low electrical resistivity (sheet resistance less than 0.02 ohm-cm) are used to deposit the films for C-V measurement. Front contact mode is used to form electrical contacts to the film. Liquid metal (mercury) is pushed out through a thin tube from a reservoir to the surface of the wafer to form two electrically conductive contacts. The contact areas are calculated based on the diameter of the tube from which the mercury is pushed out. The dielectric constant is then calculated from the formula: 

\[ k = \frac{\text{the capacitance} \times \text{the contact area}}{\text{the thickness of the film}}. \]

Example 1

The Chemical Compositions Of The Film

[0041] A typical XPS of the highly uniform nitrogen free films is shown in Figure 3 and the composition of the different elements including nitrogen is also listed in Table 1. As can be seen from both Figure 3 and Table 1, no significant nitrogen is detected in the films.

<table>
<thead>
<tr>
<th>Elements</th>
<th>O</th>
<th>Si</th>
<th>C</th>
<th>N</th>
</tr>
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<tr>
<td>Concentration</td>
<td>70.4</td>
<td>20.5</td>
<td>7.9</td>
<td>1.2</td>
</tr>
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</table>

Example 2

The Thickness And Etch Rate Of The Film

[0042] The silicon dioxide films formed using the said invention are measured for their thickness using an ellisopmeter. The film was etched in a 1 wt% HF solution along with standard thermal silicon dioxide. The etch rate was then calculated from film thickness measurements and compared with that of thermal oxide. The result is listed in Table 2. As can be seen from Table 2, the wet etch rate of the low etch rate SiO₂ (Present Invention) is much lower than even thermal oxide film.

<table>
<thead>
<tr>
<th>Material</th>
<th>After 15 second etch</th>
<th>After 1 minute etch</th>
<th>After 2 minute etch</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal SiO₂</td>
<td>43.2</td>
<td>27.6</td>
<td>29.4</td>
</tr>
<tr>
<td>Low etch SiO₂ (Present Invention)</td>
<td>1.2</td>
<td>1.2</td>
<td>1.2</td>
</tr>
</tbody>
</table>

Example 3

Silicon Oxide Deposition

[0043] Using an ATV PE0612 brand low pressure chemical vapor deposition (LPCVD) reactor we loaded twenty five 100 mm Si wafers per deposition run. Upon closing the door to the reactor, we would start the automatic system sequencer to begin the process. The sequence would initially pump the chamber down to a base pressure of ~1 - 5 mtorr (0.13 to 0.67 Pa) for about 10 minutes. Then the process would introduce nitrogen purge gas (~40 to 50 sccm) and begin control the chamber pressure using a throttle valve to obtain the proper process pressure (125 to 500 mtorr (17 to 67 Pa)). The process would then ramp the chamber up to the desired process temperature (550°C or 600°C), this would take approx-
immediately 30 minutes. Upon reaching the process temperature, the wafer temperature was allowed to stabilize for 30 minutes, while still allowing the nitrogen purge gas to flow and maintaining process pressure. After the stabilization period was complete, the nitrogen purge gas flow was shut off and the precursor (Bis(isopropylamino)vinylmethylsilane) and the oxygen began to flow while still maintaining the process pressure and temperature. The precursor (Bis(isopropylamino)vinylmethylsilane) flow was ~ 14 to 19 sccm. The oxygen flow was 20 sccm or 40 sccm. After the deposition step was completed, the precursor (Bis(isopropylamino)vinylmethylsilane) and the oxygen were turned off and the nitrogen purge gas was allowed to flow. The throttle valve was set to open to allow for purging of the chamber of the precursor (Bis(isopropylamino)vinylmethylsilane) and oxygen. At this point the system began to ramp the process chamber down to -60°C for unloading of the wafers. The ramp normally takes about 60 minutes to cool down. Upon completion of the process, the system will automatically backfill the chamber with nitrogen and bring it up to atmospheric pressure, and the door to chamber can be opened for unloading the wafers.

Using a Rudolph FOCUS Automatic Ellipsometer, 3 to 20 wafers per deposition were measured. A nine point measuring pattern was used for each wafer. The average film thickness measured varied from 350Å to 1000Å, depending upon the process used. That means that the deposition rate varied from 5.8Å/min. to 17Å/min. Wafer thickness uniformity varied from 2.0% to 6.9%. The average refractive index measured varied from 1.4375 to 1.4828.

Figure 4 shows a Dynamic Secondary Ion Mass Spectroscopy (SIMS) depth profile elemental analysis of a silicon oxide film, which indicates the excellent composition uniformity of the film for silicon, oxygen, and carbon, as well as very low content of nitrogen.

**Claims**

1. A method of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, wherein the method is a low pressure, thermal chemical vapor deposition method, atomic layer deposition method or cyclic chemical vapor deposition method, and comprises:

   a. delivering a first precursor that provides a source for silicon to a low pressure, thermal chemical vapor deposition, an atomic layer deposition or a cyclic chemical vapor deposition reactor, wherein the first precursor is selected from the group consisting of: R\(^1\)\(_{n}\)R\(^2\)\(_{m}\)Si(NR\(^3\)R\(^4\))\(_{4-n-m}\) and a cyclic silazane of the formula (R\(^1\)R\(^2\)SiNR\(^3\))\(_{p}\); where R\(^1\) is a C\(_2\)-C\(_{10}\) alkenyl or aromatic group; R\(^2\) is H, a C\(_1\)-C\(_{10}\) linear, branched, or cyclic alkyl, or an aromatic group; R\(^3\) and R\(^4\) are each selected from H, a C\(_1\)-C\(_{10}\) linear, branched, or cyclic alkyl, or an aromatic group, or are joined such that NR\(^3\)R\(^4\) is a cyclic amine; and n=1 to 3, m=0 to 2, and p=3 or 4; and

   b. delivering a second precursor that provides a source for oxygen to the reactor.

2. A low pressure, thermal chemical vapor deposition method according to Claim 1 of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, the method comprising:

   a. delivering a first precursor that provides a source for silicon to a low pressure, thermal chemical vapor deposition reactor, wherein the first precursor is selected from the group consisting of: R\(^1\)\(_{n}\)R\(^2\)\(_{m}\)Si(NR\(^3\)R\(^4\))\(_{4-n-m}\) and a cyclic silazane of the formula (R\(^1\)R\(^2\)SiNR\(^3\))\(_{p}\); where R\(^1\) is a C\(_2\)-C\(_{10}\) alkenyl or aromatic group; R\(^2\) is H, a C\(_1\)-C\(_{10}\) linear, branched, or cyclic alkyl, or an aromatic group; R\(^3\) and R\(^4\) are each selected from H, a C\(_1\)-C\(_{10}\) linear, branched, or cyclic alkyl, or an aromatic group, or are joined such that NR\(^3\)R\(^4\) is a cyclic amine; and n=1 to 3, m=0 to 2, and p=3 or 4; and

   b. delivering a second precursor that provides a source for oxygen to the reactor;

   c. reacting the first and second precursors at a temperature of 400°C to 700°C and under a pressure of 13 to 130 Pa (100mT to 1T).

3. An atomic layer deposition method according to Claim 1 of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, the method comprising:

   a. delivering a first precursor that provides a source for silicon to an atomic layer deposition reactor, wherein the first precursor is selected from the group consisting of: R\(^1\)\(_{n}\)R\(^2\)\(_{m}\)Si(NR\(^3\)R\(^4\))\(_{4-n-m}\) and a cyclic silazane of the formula (R\(^1\)R\(^2\)SiNR\(^3\))\(_{p}\); where R\(^1\) is a C\(_2\)-C\(_{10}\) alkenyl or an aromatic group; R\(^2\) is H, a C\(_1\)-C\(_{10}\) linear, branched, or cyclic alkyl, or an aromatic group; R\(^3\) and R\(^4\) are each selected from H, a C\(_1\)-C\(_{10}\) linear, branched, or cyclic alkyl, or an aromatic group, or are joined such that NR\(^3\)R\(^4\) is a cyclic amine; and n=1 to 3, m=0 to 2, and p=3 or 4; and

   b. delivering a second precursor that provides a source for oxygen to the reactor;
group, or are joined such that \( NR^3R^4 \) is a cyclic amine; and \( n=1 \) to \( 3 \), \( m=0 \) to \( 2 \), and \( p=3 \) or \( 4 \);
b. purging the reactor with an inert gas;
c. delivering a second precursor that provides a source for oxygen to the reactor;
d. purging the reactor with an inert gas;
e. repeating steps (a) to (d) until a desired thickness of the film is achieved.

4. A cyclic chemical vapor deposition method according to Claim 1 of forming a silicon dioxide film that has extremely low wet etch rate in HF solution, the method comprising:

a. delivering a first precursor that provides a source for silicon to a cyclic chemical vapor deposition reactor, wherein the first precursor is selected from the group consisting of: \( R^1_nR^2_mSi(NR^3R^4)_{4-n-m} \) and a cyclic silazane of the formula \( (R^1R^2SiNR^3)_p \); where \( R^1 \) is a \( C_2-C_{10} \) alkenyl or an aromatic group; \( R^2 \) is \( H \), a \( C_1-C_{10} \), linear, branched, or cyclic alkyl, a \( C_2-C_{10} \) linear, branched, or cyclic alkenyl, or an aromatic group; \( R^3 \) and \( R^4 \) are each selected from \( H \), a \( C_1-C_{10} \), linear, branched, or cyclic alkyl, a \( C_2-C_{10} \) linear, branched, or cyclic alkenyl, and an aromatic group, or are joined such that \( NR^3R^4 \) is a cyclic amine; and \( n=1 \) to \( 3 \), \( m=0 \) to \( 2 \), and \( p=3 \) or \( 4 \);
b. purging the reactor with an inert gas for 0.1 to 1 seconds;
c. delivering a second precursor that provides a source of oxygen to the reactor;
d. purging the reactor with an inert gas for 0.1 to 1 seconds;
e. repeating steps (a) to (d) until a desired thickness of the film is achieved.

5. The method of Claim 3 or 4, wherein the inert gas is \( Ar \) or \( N_2 \).

6. The method of any preceding claim, wherein \( R^1 \) is selected from the group consisting of vinyl, allyl, and phenyl.

7. The method of any preceding claim, wherein \( R^2 \) is selected from the group consisting of methyl, vinyl, \( H \) and allyl.

8. The method of any preceding claim, wherein \( R^3 \) and \( R^4 \) are each selected from the group consisting of isopropyl, t-butyl, ethyl, methyl and \( H \), or are joined such that \( NR^3R^4 \) is piperidino or pyrrolidino.

9. The method of any preceding claim, wherein the first precursor is Bis(isopropylamino)vinylmethylsilane.

10. The method of any preceding claim, wherein the second precursor is selected from the group consisting of oxygen, ozone and mixtures thereof.
### DOCUMENTS CONSIDERED TO BE RELEVANT

<table>
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<th>Category</th>
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<td>EP 2 228 465 A1 (AIR PROD &amp; CHEM [US]) &lt;br&gt; 15 September 2010 (2010-09-15) &lt;br&gt; * paragraphs [0004], [0005], [0013], [0034], [0067] * &lt;br&gt; -----</td>
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**The present search report has been drawn up for all claims**

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<td>The Hague</td>
<td>9 May 2012</td>
<td>Kudelka, Stephan</td>
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**CATEGORY OF CITED DOCUMENTS**

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**09-05-2012**

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